

ABSTRACT OF THE DISCLOSURE

DATA PROCESSING METHOD AND SYSTEM FOR SIMULATION OF
HARDWARE FAULTS UTILIZING A PCI BUS

1 A data processing system and method are disclosed for
2 simulating a hardware fault occurring on an expansion card
3 coupled to the system utilizing a bus. The hardware fault
4 to simulate is specified. A determination is made of a
5 signal to output utilizing the bus to simulate the hardware
6 fault occurring on the expansion card. The signal is
7 output during operation of the expansion card such that the
8 hardware fault occurring on the expansion card is
9 simulated.

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